Patent
Attorney's Docket No. 022500-021

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Jaroslav BĚLONOŽNÍK) Group Art Unit: Unassigned
Application No.: Unassigned) Examiner: Unassigned
Filed: February 19, 2002)
For: Control Unit With PCI and SCSI Buses and Computing System with Electronic Semiconductor Disc)

10081194.022502

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination, please amend claims 2-8 in the above-identified application as shown on the attached sheets:

REMARKS

In this preliminary amendment claims 2-8 have been amended to remove multiple dependencies and to place the claims in the preferred U.S. format.

A prompt and favorable examination of the claims is earnestly solicited. If the Examiner has any questions concerning the amendment or the above identified application

in general, the Examiner is invited to contact the undersigned so as to expedite prosecution.

Respectfully submitted,

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Date: 2/25/02

10081494.022502

Attachment to Preliminary Amendment dated February 19, 2002

Marked-up Claims 2-8

2. (Amended) Use of a control unit according to claim 1, [where] wherein the electronic semiconductor disc includes a processor and semiconductor memory, selected from a group comprising dynamic memory, synchronous dynamic memory, static memory and flash type memory.

3. (Amended) A computing system with electronic semiconductor disc with processor [(3), characterised in that], wherein the processor [(3)] of the computing system [(1)] is connected by a PCI bus [(8)] to a PCI adapter [(4)], which is linked through the PCI bus [(8)] to the semiconductor memory [(5)], connected by the local bus [(9)] to a processor [(3)], where the PCI adapter [(4)] comprises a unit [(11)] of the programmable SCSI control unit, connected both to the interface [(13)] of the PCI bus for communicating with the electronic semiconductor disc, and to the interface [(12)] of the SCSI bus for communicating with the external computing system [(2)] with the SCSI control unit [(6)].

4. (Amended) A computing system with electronic semiconductor disc according to claim 3, [characterised in that] wherein the semiconductor memory [(5)] consists of a synchronous dynamic SDRAM memory.

5. (Amended) A computing system with electronic semiconductor disc according to [claims 3 or 4, characterised in that] claim 3, wherein it further comprises a magnetic disc [(10)] and/or a unit [(15)] for standby power supply.

6. (Amended) A computing system with electronic semiconductor disc according to [any of claims 3 to 5, characterised in that] claim 3, wherein the PCI adapter [(4)] further contains a memory unit [(14)] which is connected to the unit [(11)] of the programmable SCSI control unit in the PCI adapter [(4)] and/or to the interface [(13)] of the PCI bus for communicating with the electronic semiconductor disc.

7. (Amended) A computing system with electronic semiconductor disc according to claim 6, [characterised in that] wherein the memory unit [(14)] of the PCI adapter [(4)] consists of a programmable EPROM, PEROM, EEPROM or flash EPROM memory.

8. (Amended) A computing system with electronic semiconductor disc according to [any of claims 3 to 7, characterised in that] claim 3, wherein it is connected by the SCSI bus [(7)] to the external computing system [(2)].

Attachment to Preliminary Amendment dated February 19, 2002

Clean Copy of Claims 2-8

2. (Amended) Use of a control unit according to claim 1, wherein the electronic semiconductor disc includes a processor and semiconductor memory, selected from a group comprising dynamic memory, synchronous dynamic memory, static memory and flash type memory.

3. (Amended) A computing system with electronic semiconductor disc with processor, wherein the processor of the computing system is connected by a PCI bus to a PCI adapter, which is linked through the PCI bus to the semiconductor memory, connected by the local bus to a processor, where the PCI adapter comprises a unit of the programmable SCSI control unit, connected both to the interface of the PCI bus for communicating with the electronic semiconductor disc, and to the interface of the SCSI bus for communicating with the external computing system with the SCSI control unit.

4. (Amended) A computing system with electronic semiconductor disc according to claim 3, wherein the semiconductor memory consists of a synchronous dynamic SDRAM memory.

5. (Amended) A computing system with electronic semiconductor disc according to claim 3, wherein it further comprises a magnetic disc and/or a unit for standby power supply.

6. (Amended) A computing system with electronic semiconductor disc according to claim 3, wherein the PCI adapter further contains a memory unit which is connected to the unit of the programmable SCSI control unit in the PCI adapter and/or to the interface of the PCI bus for communicating with the electronic semiconductor disc.

7. (Amended) A computing system with electronic semiconductor disc according to claim 6, wherein the memory unit of the PCI adapter consists of a programmable EPROM, PEROM, EEPROM or flash EPROM memory.

8. (Amended) A computing system with electronic semiconductor disc according to claim 3, wherein it is connected by the SCSI bus to the external computing system.